

TRICON V9 MAIN PROCESSOR

Product Data Sheet

3006

Main Processor Module

SYSTEM	SRAM	TRIBUS	POWER
v9.0-v9.5.x	2 MB	4 Mbit/s	15 W

Triconex 3006 is a Main Processor Module for Tricon v9.0-v9.5.x systems. Three processor modules operate as independent controller channels and communicate through the fault-tolerant Tricon architecture.

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VERIFIED TECHNICAL DATA

Product type	Main Processor Module
System versions	Tricon v9.0-v9.5.x
Central processor	National NS32GX32, 32-bit, 25 MHz
EPROM memory	512 KB
SRAM	2 MB
TriBus	4 Mbit/s, 16-bit DMA
Serial port	Optically isolated RS-232, DB25, 500 VDC isolation
Communication interface	RS-485, 2 Mbit/s

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ADDITIONAL VERIFIED DATA

I/O interface	RS-485, 375 kbit/s
Logic power	15 W
Slot keying	Main Processor slot keys: top 007, bottom 007
Source	Tricon v9-v10 Planning and Installation Guide

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ENGINEERING NOTES

- Install only in a keyed Tricon Main Processor slot.
- Confirm the controller is a documented Tricon v9.0-v9.5.x system.
- Match firmware among the three Main Processor modules before returning the controller to service.
- Use the documented diagnostic and communication connectors only.
- Verify PASS, FAULT, ACTIVE, MAINT1 and MAINT2 indications.
- Confirm execution of the control program and communication across the I/O and communication buses.
- Review the Tricon event log after replacement or re-education.
- Confirm cabinet temperature and power conditions are within system requirements.

Verify the exact model, slot keying, field wiring and controlled application before replacement.