MFR3AD63 PMC

Product Data Sheet

835675-051/MFR3AD63

FPGA-based PMC module

FPGA DEVICE	STATIC RAM	EXTERNAL CONNECTOR	STATUS INDICATION
Virtex XCV600	**2 MB**	**50-pin SCSI**	**On-board LEDs**

EMS 835675-051/MFR3AD63 is an FPGA-based PMC module containing a Virtex XCV600 device and local static memory. The board is a mezzanine assembly intended for a compatible carrier arrangement rather than a standalone enclosed controller.

01 TECHNICAL DATA

FPGA device	Virtex XCV600
Static RAM	2 MB
External connector	50-pin SCSI
Status indication	On-board LEDs

02 ADDITIONAL PRODUCT DATA

The identified hardware includes 2 MB of static RAM, status LEDs and a 50-pin SCSI connector. These component and connector details describe the board assembly; the SCSI connector format alone does not establish a SCSI storage protocol, and the loaded FPGA application is not determined by the physical device designation.

03 PRODUCT FUNCTION

Use the MFR3AD63 as a PMC mezzanine assembly with the intended host carrier. Compare the physical carrier connection and fitted board hardware before integration; the XCV600 FPGA identification alone does not define the host application or external signal assignment.

04 SYSTEM COMPATIBILITY

The board provides 2 MB of static RAM and a 50-pin SCSI-format external connector, together with status LEDs. Identify the actual cable pin assignment from the equipment documentation, because a connector shell type is not evidence of a storage protocol or generic signal compatibility.

05 DOCUMENT BASIS

Technical reference compiled for the exact recorded assembly. Manufacturer documentation is not included.

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