SPEEDTRONIC MARK V

Product Data Sheet

DS200TCQCG1BJF

Analog I/O expander board

CORE POWER CONNECTOR	STCA INTERFACES	TCPS DIAGNOSTIC INTERFACE	TCQA INTERFACE
2PL	**8PL and 19PL**	**JC**	**JE**

GE DS200TCQCG1BJF is a Speedtronic Mark V analog I/O expander board in the TCQC family. It conditions additional analog signals and provides excitation for LVDT and LVDR position sensors.

01 TECHNICAL DATA

Core power connector	2PL
STCA interfaces	8PL and 19PL
TCPS diagnostic interface	JC
TCQA interface	JE
IONET termination connector	JX
Servo output connector	JFF
Pulse input interface	JH
LVDT/LVDR excitation	3.2 kHz, 7 V RMS
Software configuration	None

GE GEH-6153 Function Manual, WINSYS Edition, pages 4-23 to 4-24

02 ADDITIONAL PRODUCT DATA

The TCQC exchanges signals with STCA and TCQA and routes servo outputs to the QTBA terminal board. Hardware jumpers configure servo feedback scaling and output current. The Mark V LM manual documents no separate TCQC software configuration.

03 PRODUCT FUNCTION

Check the core drawing to identify the connections for the installed TCQC. Match the 2PL power feed, STCA interfaces and TCQA interface to the marked harnesses.

04 SYSTEM COMPATIBILITY

Compare the original servo jumper settings with the approved output range and feedback scaling. Use the GE hardware jumper table for the relevant core and servo channel.

05 REFERENCE

GE GEH-6153 Function Manual, WINSYS Edition, pages 4-23 to 4-24

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