SPEEDTRONIC MARK V

Product Data Sheet

DS215TCDAG1BZZ01A DS200TCDAH1BGD

Digital contact I/O board

DS200 CONTACT INPUT FROM DTBA	DS200 CONTACT INPUT FROM DTBB	DS200 CORE POWER CONNECTION	DS200 CONTACT OUTPUT CONNECTIONS
JQ	**JR**	**JP**	**JO1 and JO2**

The GE DS200TCDAH1BGD is a Speedtronic Mark V TCDA digital I/O board. It conditions contact inputs from DTBA and DTBB terminal boards and sends contact output signals to TCRA boards.

01 TECHNICAL DATA

DS200 contact input from DTBA	JQ
DS200 contact input from DTBB	JR
DS200 core power connection	JP
DS200 contact output connections	JO1 and JO2
DS200 IONET connections	JX1 or JX2
DS200 hardware jumpers	8
DS200 IONET termination jumpers	J2 and J3
DS200 IONET ID jumpers	J4 through J6
DS200 stall timer jumper	J7

GEH-6153, chapter 4, TCDA

02 ADDITIONAL PRODUCT DATA

DS215TCDAG1BZZ01A is a separately identified Mark V TCDA assembly. The specifications below describe the DS200 board. The complete board marking and fitted firmware must be verified for the DS215 assembly before treating either ordering code as a replacement.

03 PRODUCT FUNCTION

Match the installed digital I/O core to the applicable Mark V drawing. On the DS200 board, JQ receives DTBA signals and JR receives DTBB signals.

04 SYSTEM COMPATIBILITY

Confirm the TCPS power connection at JP and the installed IONET routing at JX1 or JX2. The JO1 and JO2 output paths depend on the core layout.

05 REFERENCE

GEH-6153, chapter 4, TCDA

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