GE MARK VIE

Product Data Sheet

IS210SCLSH1A

Core Analog Terminal Board

COMPATIBLE I/O PACKS	THERMOCOUPLE CHANNELS	THERMOCOUPLE TERMINAL RANGE	ANALOG OUTPUT CHANNEL
IS220PCLAH1A / IS220PCLAH1B	**TC1–TC8**	**TB1B.25–TB1B.40**	**OP1 / OR1**

The SCLS board is the core analog termination accessory used with the GE Mark VIe PCLA pack.

01 TECHNICAL DATA

Compatible I/O packs	IS220PCLAH1A / IS220PCLAH1B
Thermocouple channels	TC1–TC8
Thermocouple terminal range	TB1B.25–TB1B.40
Analog output channel	OP1 / OR1
Analog output terminal pair	TB1A.21 / TB1A.22
Field terminal style	Euro-style box terminals
Thermocouple input range with PCLA	–16 to +63 mV DC
Analog output range with PCLA	0–20 mA DC

GEH-6725R Instruction Guide, section 3.4

02 ADDITIONAL PRODUCT DATA

The GE field-terminal schedule identifies eight thermocouple pairs on TB1B and the first analog output pair on TB1A. Optional SCLT termination extends the documented channel allocation.

03 PRODUCT FUNCTION

The GE field-terminal schedule identifies eight thermocouple pairs on TB1B and the first analog output pair on TB1A. Optional SCLT termination extends the documented channel allocation.

04 SYSTEM COMPATIBILITY

Use the GE SCLS field-terminal schedule to identify TC1–TC8 on TB1B. Confirm that the first analog output is assigned to TB1A.21 and TB1A.22.

05 REFERENCE

GEH-6725R Instruction Guide, section 3.4

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