HIQUAD H41Q CENTRAL PROCESSING

Product Data Sheet

F8652X

Redundant safety central module

PROCESSOR ARRANGEMENT	PROCESSOR ARCHITECTURE	CLOCK FREQUENCY	OPERATING-SYSTEM MEMORY PER CPU
Two clock-synchronized microprocessors	**Intel 386EX, 32-bit**	**25 MHz**	**1 MB Flash-EPROM**

HIMA F8652X is a safety-related central module for the H41q-MS, H41q-HS and H41q-HRS programmable electronic arrangements. Two clock-synchronized Intel 386EX processors execute the central processing functions, with manufacturer-stated applicability up to SIL 3 according to IEC 61508. A four-digit diagnostic display and CPU/IO indications expose detailed operating information.

01 TECHNICAL DATA

Processor arrangement	Two clock-synchronized microprocessors
Processor architecture	Intel 386EX, 32-bit
Clock frequency	25 MHz
Operating-system memory per CPU	1 MB Flash-EPROM
User-program memory per CPU	1 MB Flash-EPROM; utilization depends on OS
Data memory per CPU	1 MB SRAM; utilization depends on OS
Serial connections	Two electrically isolated RS-485 interfaces
Diagnostic indication	Four-digit matrix display with CPU and IO LEDs
Watchdog output	24 V, 500 mA maximum, short-circuit proof
Rack space	8 SU

02 ADDITIONAL PRODUCT DATA

Internal operating supply	5 V, 2 A
Serial station setting	1-31
Ethernet station extension	1-99 with F8627X and BS41q/51q V7.0-8 or later
Safety applicability	Up to SIL 3 according to IEC 61508
Unpowered battery retention	1000 days at 25 C; 200 days at 60 C

03 PRODUCT FUNCTION

Each processor runs at 25 MHz with separate 1 MB operating-system Flash-EPROM, 1 MB user-program Flash-EPROM and 1 MB data SRAM. The assembly provides two electrically isolated RS-485 interfaces and a short-circuit-proof 24 V watchdog output rated up to 500 mA. Ethernet communication is provided through the separate F8627X communication module over the backplane, rather than an onboard Ethernet socket.

04 SYSTEM COMPATIBILITY

When communication uses F8627X, data pass between F8652X and that separate module over the backplane. Station numbers 1-99 and ELOP II TCP support require BS41q/51q V7.0-8 (05.31) or later, while the serial station setting remains 1-31. Do not infer onboard Ethernet merely from support for an Ethernet-connected configuration.

05 REFERENCE

HIMA F 8652X (1141), pages 1-4.

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