AC500-S

Product Data Sheet

SM560-S-FD-4

Safety processor module

SAFETY INTEGRITY LEVEL	PERFORMANCE LEVEL	USER PROGRAM MEMORY	INTEGRATED DATA MEMORY
SIL 3	**PL e**	**1.3 MB**	**1 MB**

ABB SM560-S-FD-4 is an AC500-S safety CPU with 1.3 MB program memory, designed for the documented SIL 3 and PL e safety architecture. It operates alongside a compatible non-safety AC500 CPU and exchanges safety data using PROFIsafe.

01 TECHNICAL DATA

Safety integrity level	SIL 3
Performance level	PL e
User program memory	1.3 MB
Integrated data memory	1 MB
Saved-data capacity	120 kB
Binary instruction time	0.05 us
Word instruction time	0.06 us
Floating-point instruction time	0.5 us
Maximum local safety I/O modules	10
Maximum local safety digital inputs	160 at SIL2; 80 at SIL3

02 ADDITIONAL PRODUCT DATA

Maximum local safety digital outputs	80 at SIL3
Maximum local safety analog inputs	40 at SIL2; 20 at SIL3
Safety protocol	PROFIsafe V2
F-Device network capacity	4
Ingress protection	IP20 in complete closed assembly

03 PRODUCT FUNCTION

This F-Device variant adds connectivity to 4 PROFIsafe networks while retaining F-Host capability. Its 1.3 MB program memory, safety I/O limits and compatible non-safety CPU firmware must be considered together when configuring the safety project.

04 SYSTEM COMPATIBILITY

This F-Device variant adds connectivity to 4 PROFIsafe networks while retaining F-Host capability. Its 1.3 MB program memory, safety I/O limits and compatible non-safety CPU firmware must be considered together when configuring the safety project.

05 REFERENCE

ABB AC500-S Safety User Manual 3ADR025091M0211

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