MANUFACTURER DOCUMENT PACKAGE

# Manufacturer's Manual

Reference package for procurement and maintenance review

GENERAL ELECTRIC

## IC694MDL660

**32-POINT 24 V DC INPUT MODULE**

Used with the applicable PACSystems RX3i configuration.

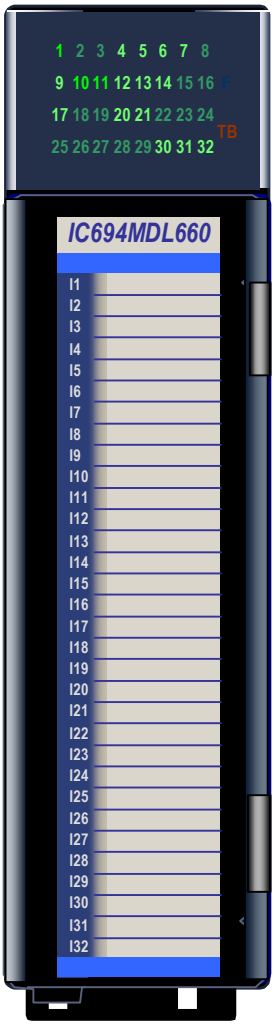
REFERENCE PACKAGE

OFFICIAL DOCUMENT

Selected official manufacturer pages follow this cover.

Source: GE PACSystems RX3i discrete I/O manual

# *Input Module, 24 VDC 32 Point Grouped: IC694MDL660*




The ***24 VDC Positive/Negative Logic Input*** module, IC694MDL660, provides 32 discrete input points. The inputs are positive or negative logic inputs and will operate at levels up to 30V.

The inputs are arranged in four isolated groups of eight; each group has its own common. Isolation is provided between the four groups of inputs, however each group of eight inputs is referenced to the same user common connection.

Module MDL660 provides seven selectable input filter times. Filter times can be set from the programmer using the module's assigned output data references.

This module can be used with either a Box-style (IC694TBB032) or Spring-style (IC694TBS032) front Terminal Block. The Terminal Block is ordered separately.

32 green LEDs indicate the ON/OFF status of points 1 through 32. The red/green TB LED is green when the module's removable terminal block is locked in place. It is red when the terminal block is not locked. The module also sends an *Addition of Terminal Block* or *Loss of Terminal Block* message to the RX3i CPU to report the terminal block status.

The blue bands on the label show that MDL660 is a low-voltage module.

This module can be installed in any I/O slot in an RX3i system. It must be used with an RX3i CPU (release 2.90 or later). It cannot be used with a Series 90-30 PLC CPU.

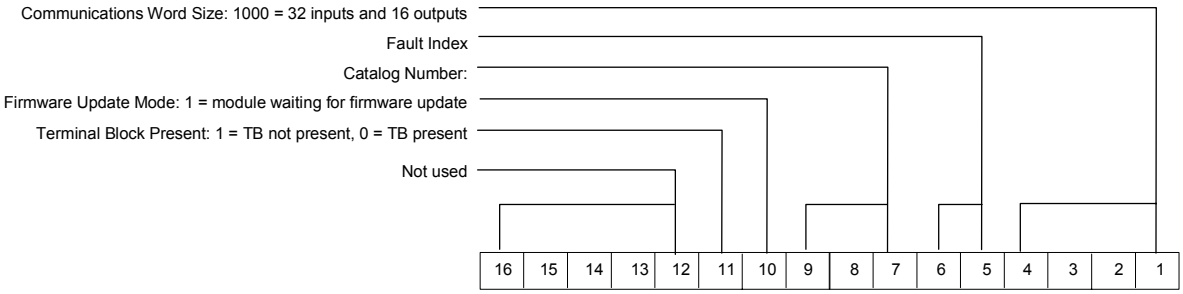
Module MDL660 uses 48 input bits and 16 output bits to exchange point status and filter information with the RX3i CPU.

## Module Data: IC694MDL660

Discrete Input Module IC694MDL660 uses 48 input reference bits and 16 output reference bits in the CPU.

### Input Data: MDL660

The module uses the first 11 input bits to report its status information to the RX3i CPU. It has the following content:




Bits 12 – 16 are not used.

The CPU uses the information contained in these input bits to uniquely identify the module, and to monitor its Board Ready and Terminal Block status.

The module reports input point data in bits 17 - 48.

| ***Input Status Bits*** | | | |
|---|---|---|---|
| *Input Group 1* | *Input Group 2* | *Input Group 3* | *Input Group 4* |
| Bits 17-24 | Bits 25-32 | Bits 33-40 | Bits 41-48 |

### Output Data: MDL660

The module receives 16 bits of output data from the RX3i CPU. Bits 0- 8 contains the filter time configuration data as shown below. Bits 9 - 16 are not used.

| ***Binary Value in the Output Reference Bits*** | ***Filter Time*** |
|---|---|
| 0000 | 0.5ms |
| 0001 | 1ms |
| 0011 | 2ms |
| 1001 | 5ms |
| 10011 | 10ms |
| 1100011 | 50ms |
| 11000111 | 100ms |