

Main Processor Front Panel

This figure depicts the Main Processor front panels for models 3006, 3007, and 3008.

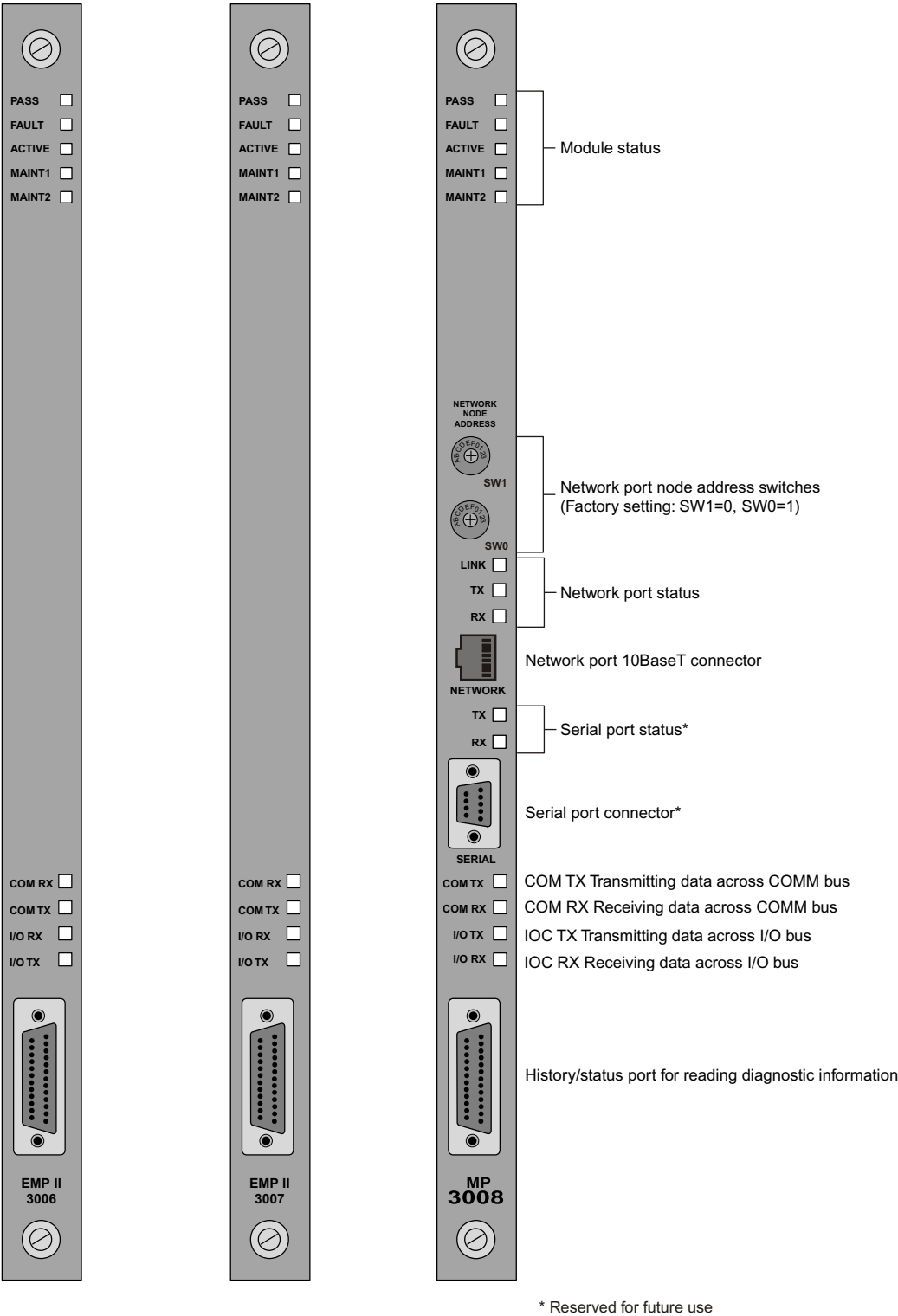


Figure 14 3006, 3007, and 3008 Main Processor Front Panels

3008 Main Processor Architecture

This figure depicts the model 3008 Main Processor architecture, which can be used with Tricon v9.6 and later systems.

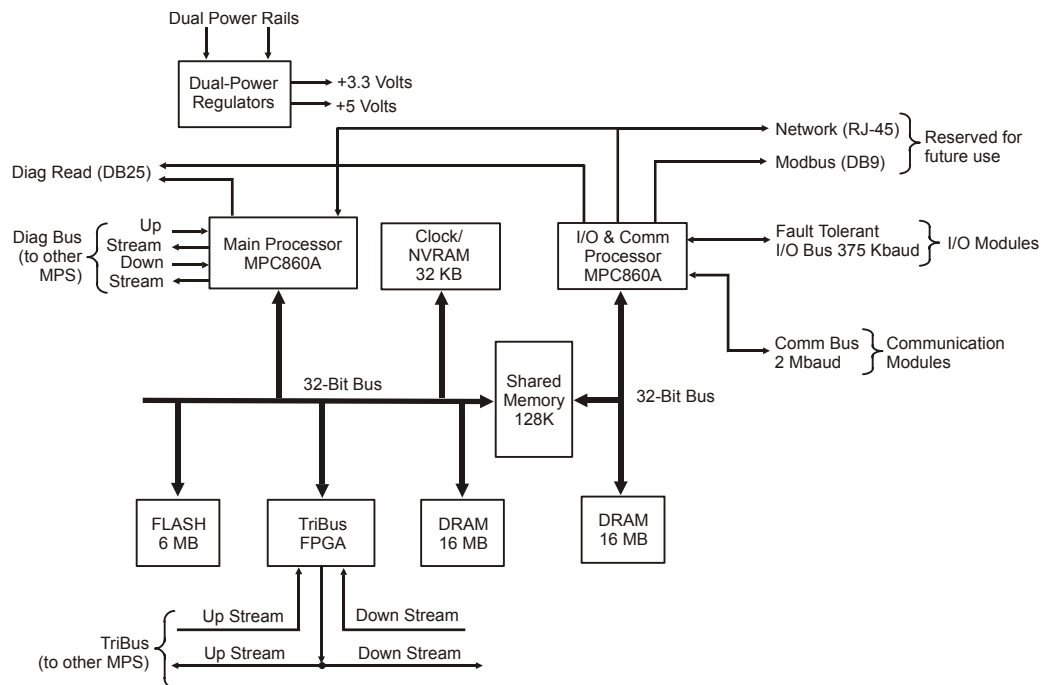


Figure 15 3008 Architecture

3008 Specifications

This table lists the specifications for the model 3008 Main Processor Module, which is available with Tricon v9.6 and later systems.

Table 8 3008 Main Processor Specifications

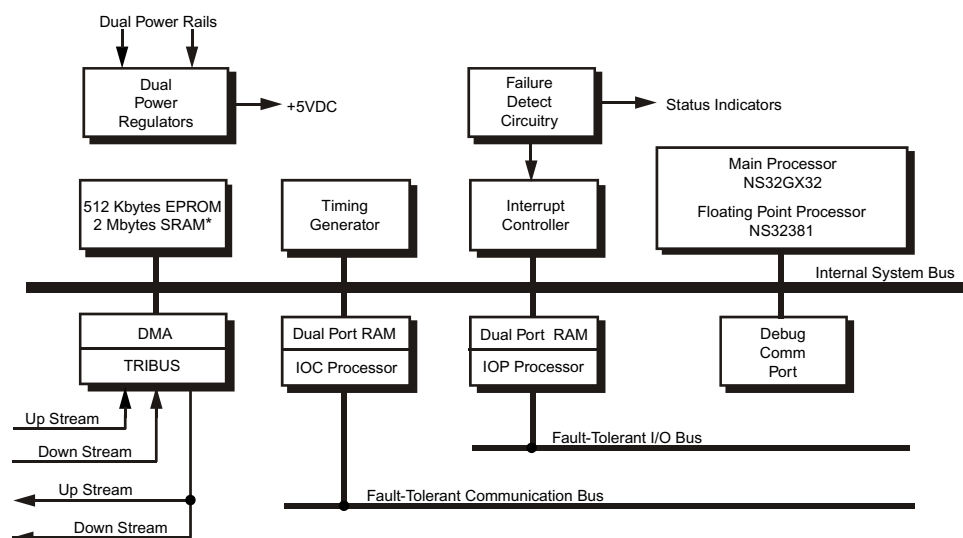
Feature	Description
Main processor	Motorola MPC860, 32-bit, 50 MHz
Memory	16 MB DRAM (without battery back-up) 32 KB SRAM (with battery back-up) 6 MB Flash PROM
TriClock	Time and date Battery back-up Typical drift: ± 2 seconds/day Maximum drift: ± 2.16 seconds/day
TriBus	25 megabits/second 32-bit CRC-protected 32-bit DMA, fully isolated

Table 8 3008 Main Processor Specifications

Feature	Description
Serial port	For diagread diagnostic analysis Optically isolated RS-232 interface on one 25-pin connector 500 VDC isolation
Communication processor	Motorola MPC860, 50 MHz, 32-bit
Communication interface	
Protocol	RS-485
Baud rate	2 megabits per second
I/O interface	
Protocol	RS-485
Baud Rate	375 kilobits per second
Logic power	10 W

3006 and 3007 Main Processor Architecture

This figure depicts the model 3006 and 3007 Main Processor architecture, which can be used with Tricon v9.0 – v9.5 systems. They have the same architecture and specifications, except for SRAM, which is 2 megabytes for the 3006 and 1 megabyte for the 3007.

**Figure 16 3006 and 3007 Main Processor Architecture**