LED Operation

The following table lists the CPU LED functions during normal operation (after initialization sequence is complete).

LED State			CPU Operating State
● On	✿ Blinking	○ Off	
●	**CPU OK**	On	CPU has passed its powerup diagnostics and is functioning properly.
○	**CPU OK**	Off	CPU problem. RUN and OUTPUTS ENABLED LEDs may be blinking in an error code pattern, which can be used by technical support for troubleshooting. This condition and any error codes should be reported to your technical support representative.
✿	**CPU OK**, **OUTPUTS ENABLED**, **RUN**	Blinking in unison	CPU is in boot mode and is waiting for a firmware update through serial port.
●	**RUN**	On	CPU is in Run mode
○	**RUN**	Off	CPU is in Stop mode.
●	**OUTPUTS ENABLED**	On	Output scan is enabled.
○	**OUTPUTS ENABLED**	Off	Output scan is disabled.
○	**I/O FORCE**	On	Override is active on a bit reference.
✿	**BATTERY**	Blinking	Battery is low.
●	**BATTERY**	On	Battery is dead or not attached.
●	**SYSTEM FAULT** On		CPU is in Stop/Faulted or Stop/Halted mode.
✿	**COM1** **COM2**	Blinking Blinking	Signal activity on port.

Specifications*

IC695CPU310	
Battery: Memory retention	For estimated battery life under various conditions, refer to the *PACSystems CPU Reference Manual,* GFK-2222.
Program storage	Up to 10 Mbytes of battery-backed RAM 10 Mbytes of non-volatile flash user memory
Power requirements	+3.3 VDC: 1.25 Amps nominal +5 VDC: 1.0 Amps nominal
Operating Temperature	0 to 60°C (32°F to 140°F)
Floating point	Yes
Boolean execution speed, typical	0.195ms per 1000 Boolean contacts/coils
Time of Day Clock accuracy	Maximum drift of 2 seconds per day
Elapsed Time Clock (internal timing) accuracy	0.01% maximum
Embedded communications	RS-232, RS-485
Serial Protocols supported	Modbus RTU Slave, SNP, Serial I/O
Backplane	Dual backplane bus support: RX3i PCI and 90-30-style serial
PCI compatibility	System designed to be electrically compliant with PCI 2.2 standard
Program blocks	Up to 512 program blocks. Maximum size for a block is 128KB.
Memory	*%I and %Q:* 32Kbits for discrete *%AI and %AQ:* configurable up to 32Kwords *%W:* configurable up to the maximum available user RAM Symbolic: configurable up to 10 Mbytes
* For environmental specifications and compliance to standards (for example, FCC or European Union Directives), refer to the *PACSystems RX3i Hardware and Installation Manual,* GFK-2314.	