IC693CPU352 Data

CPU Type	Single slot CPU module
Total Baseplates per System	8 (CPU baseplate + 7 expansion and/or remote)
Load Required from Power Supply	890 milliamps from +5 VDC supply
Processor Speed	25 MegaHertz
Processor Type	80386EX
Typical Scan Rate	0.22 milliseconds per 1K of logic (Boolean contacts)
User Memory (total)	240K (245,760) bytes, starting with firmware release 9.0. **Note:** Actual size of available user program memory depends on the amounts configured for the %R, %AI, and %AQ configurable word memory types (described below). Pre-9.0 firmware supported 80K bytes of fixed memory.
Discrete Input Points - %I	2,048
Discrete Output Points - %Q	2,048
Discrete Global Memory - %G	1,280 bits
Internal Coils - %M	4,096 bits
Output (Temporary) Coils - %T	256 bits
System Status References - %S	128 bits (%S, %SA, %SB, %SC - 32 bits each)
Register Memory - %R	Configurable in 128 word increments, from 128 to 16,384 words with DOS programmer, and from 128 to 32,640 words with Windows programmer Ver. 2.2 or later, VersaPro version 1.0 or later, or Logic Developer-PLC software.
Analog Inputs - %AI	Configurable in 128 word increments, from 128 to 8,192 words with DOS programmer, and from 128 to 32,640 words with Windows programmer Ver 2.2 or later, VersaPro version 1.0 or later, or Logic Developer-PLC software.
Analog Outputs - %AQ	Configurable in 128 word increments, from 128 to 8,192 words with DOS programmer, and from 128 to 32,640 words with Windows programmer Ver. 2.2 or later, VersaPro version 1.0 or later, or Logic Developer-PLC software.
System Registers (for reference table viewing only; cannot be referenced in user logic program)	28 words (%SR)
Timers/Counters	>2,000 (depends on available user memory)
Shift Registers	Yes
Built-in Serial Ports	Three (one uses connector on PLC power supply). Supports SNP/SNPX slave (on all three ports) and RTU slave and Serial I/O (on Ports 1 and 2). Requires CMM module for CCM and PCM module for RTU master.
Communications	*LAN* – Supports multidrop. Also supports Ethernet, FIP, Profibus, GBC, GCM, and GCM+ option modules.
Override	Yes
Battery Backed Clock	Yes
Interrupt Support	Supports the periodic subroutine feature.
Type of Memory Storage	RAM and Flash
PCM/CCM Compatibility	Yes
Floating Point Math Support	Yes. Hardware-based (uses built-in math coprocessor)