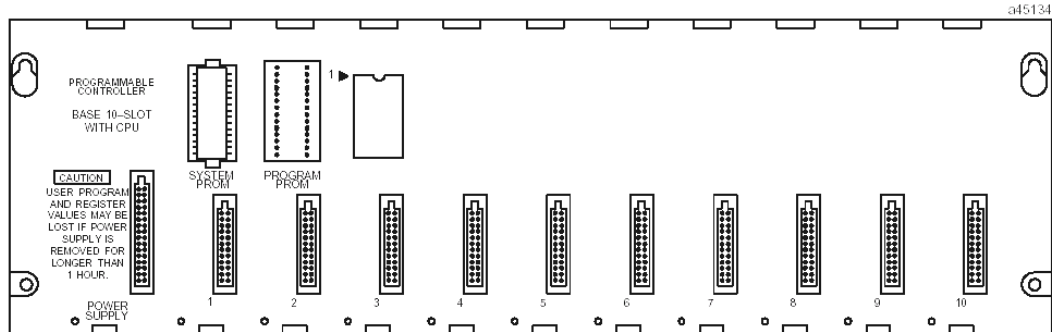


CPU323

Catalog Number IC693CPU323



| CPU Type | 10-slot baseplate with embedded CPU |
|---|---|
| Total Baseplates per System | 1 |
| Load Required from Power Supply | 430 milliamps from +5 VDC supply |
| Processor Speed | 10 MegaHertz |
| Processor Type | 80188 |
| Operating Temperature | 0 to 60 degrees C (32 to 140 degrees F) ambient |
| Typical Scan Rate | 0.6 milliseconds per 1K of logic (boolean contacts) |
| User Program Memory (maximum) | 12K Bytes (6K bytes prior to release 7) |
| Discrete Input Points - %I | 320 (maximum - combined inputs + outputs) |
| Discrete Output Points - %Q | 320 (maximum - combined outputs + inputs) |
| Discrete Global Memory - %G | 1280 bits |
| Internal Coils - %M | 1024 bits |
| Output (Temporary) Coils - %T | 256 bits |
| System Status References - %S | 128 bits (%S, %SA, %SB, %SC - 32 bits each) |
| Register Memory - %R | 1024 words |
| Analog Inputs - %AI | 64 words |
| Analog Outputs - %AQ | 32 words |
| System Registers (for reference table viewing only; cannot be referenced in user logic program) | 16 words (%SR) |
| Timers/Counters | 340 |
| Shift Registers | yes |
| Built-in Ports | 1 (uses connector on PLC power supply). Supports SNP slave and SNP-X slave protocols. |
| Communications | LAN - Supports multidrop. Also supports Ethernet, FIP, Profibus, GBC, GCM, GCM+ option modules. |
| Override | no |
| Battery Backed Clock | no |
| Interrupts | no |
| Type of Memory Storage | RAM and optional EPROM or EEPROM |
| PCM/CCM Compatibility | no |