4.4.8. Digital Input Sequence of Events

Function

The Digital Input Sequence of Events (DISOE) accepts 24VDC discrete signals as discrete inputs. The inputs can be time tagged to support 1ms resolution Sequence of Events

Notable Features

- Three modes of operation
  - Normal (20ms PV scan)
  - Sequence of Events (1ms resolution SOE, 20ms PV scan)
  - Low Latency (5ms PV scan)
- Extensive internal diagnostics for data integrity
- Optional redundancy
- Internal or external field power selection
- On board excitation power (no need for marshalling power)
- Direct / Reverse Input Indication
- Galvanic isolation

Detailed Specification – Digital Input SOE (8C-PDISA1)

Parameter	Specification		
Input / Output Module	8C-PDISA1 - Digital Input Sequence of Events, Coated		
IOTA Modules	8C-TDILA1	Non Redundant, Coated	9”
	8C-TDILB1	Redundant, Coated.	12”
Input Channels	32		
Input Channel Scanning (PV)	Normal = 20ms ; Fast = 5ms		
Digital Input Resolution for Sequence of Events (SOE)	1ms		
Voltage Rating	24 VDC		
DI Power Voltage Range	18 to 30 VDC		
Module current rating	95 mA		
Galvanic Isolation (any input terminal voltage referenced to common)	1000 VAC RMS or ±1000 VDC		
Isolation Technique	Optical (in IOM)		
ON Sense Voltage/Current	13 VDC (min) or 3 mA (min)		
OFF Sense Voltage/Current	5 VDC (max) or 1.2 mA (max)		
Input Impedance	4.2 KΩ		
Absolute Delay Across Input Filter and Isolation	5 ms ± 20%		
Field Resistance for Guaranteed ON Condition	300 Ωmax @ 15 VDC		
Field Resistance for Guaranteed OFF Condition	30 KΩmin @ 30 VDC		
Module Removal and Insertion under power	Supported		