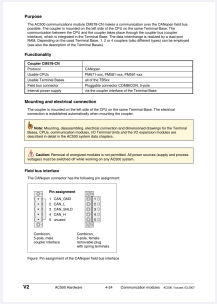
AMIKON TECHNICAL INTELLIGENCE

Product Data Sheet

ABB CM578-CN

CANopen Master Communication Module



BUS RATE	CPU FAMILIES	CONNECTOR	DUAL PORT
10 kbit/s-1 Mbit/s	**PM57x/58x/59x**	**5-pin**	**8 kByte**

ABB CM578-CN enables CANopen communication for the documented AC500 CPU families. It exchanges controller data through the terminal-base communication interface and connects to the fieldbus through a removable five-pin COMBICON connector.

01 VERIFIED PRODUCT PARAMETERS

Transmission-rate range	10 kbit/s to 1 Mbit/s
Documented CPU families	PM57x, PM58x and PM59x
Supported terminal bases	All TB5xx listed by ABB
Field connector	Pluggable five-pin COMBICON
Connector allocation	1 CAN_GND; 2 CAN_L; 3 CAN_SHLD; 4 CAN_H; 5 unused
Module power path	Via the communication-module interface of the terminal base
Typical CPU-base 24 V current	50 mA
Typical communication-bus current	290 mA
Dual-port memory	8 kByte
Internal RAM	256 kByte

Technical values were verified against ABB 3ADR010018 rev.2 (2018-05-04) and AC500 Hardware V2 CM578-CN section (03.2007).

02 ADDITIONAL TECHNICAL DATA

Firmware flash memory	512 kByte
CANopen addressing	Slave node addresses 1-127
Cable-length examples	30 m at 1 Mbit/s; 100 m at 500 kbit/s; 500 m at 125 kbit/s
Horizontal operating ambient	0-60 C for the standard AC500 version
Ingress protection	IP20

03 SYSTEM ROLE AND APPLICATIONS

Order 1SAP170800R0001 is the standard CM578-CN, distinct from the XC order. ABB datasheet revision 2 lists both variants as Classic. Preserve the installed CANopen configuration, node identities and applicable environmental conditions when evaluating a replacement.

- CANopen process-data exchange with compatible digital or analog I/O nodes.
- Device-profile-based communication with compatible drive devices.
- Acquisition of encoder information through the documented CANopen object-dictionary mechanism.

04 OFFICIAL INSTALLATION FACTS

- Isolate supply and process voltages before mounting, disassembling or changing connections.
- Fit CM578-CN on the left side of the CPU in the supported terminal-base arrangement.
- Connect CAN_GND, CAN_L, CAN_SHLD and CAN_H to pins 1-4 respectively; pin 5 is unused.
- Match transmission rate to the documented bus-length limit and place 120-ohm termination at the data-line ends.

05 OFFICIAL DOCUMENTATION

ABB 3ADR010018 rev.2 (2018-05-04) and AC500 Hardware V2 CM578-CN section (03.2007)

AMIKON PRODUCT SUPPORT

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