

AMIKON TECHNICAL DOCUMENTATION

Installation Instructions

Siemens 6ES7223-0BD30-0XB0

SIMATIC S7-1200 | SB 1223 digital input/output signal board

INPUTS	OUTPUTS	FIELD	FORMAT
2 digital	2 digital	24 V DC	Signal board

For qualified personnel. Follow the complete applicable manufacturer instructions and site safety procedures. Secure the machine and isolate affected circuits before installation, removal or wiring changes.

01

PREPARATION & CONNECTION

Power isolation	Secure the process and disconnect the CPU and all S7-1200 equipment from electrical power.
CPU preparation	Confirm a supported CPU and available signal-board slot; remove covers as instructed.
Board insertion	Insert straight into the CPU mounting position and press until the board is correctly seated.
Supply connections	X19 pin 1 is L+ and pin 2 is M; use the specified DC supply range.
Signal connections	X19 pins 3/4 are the inputs; pins 5/6 are the outputs. Match signal polarity and channel addressing.
Load protection	Respect the 0.5 A per-channel limit and provide suitable external circuit protection.

Instruction basis: M pp.45-46,1281-1283. This guide is not a substitute for a controlled installation procedure.

02

COMMISSIONING & FUNCTIONAL CHECKS

- Verify the complete order code and do not use the diagram for a 5 V or 200 kHz variant.
- Check the 20.4-28.8 V DC field supply and the polarity of all field connections.
- Configure the board, input behavior and required RUN-to-STOP output values in the project.
- Inspect the removable connector, replace covers and check for wiring shorts before energization.
- Test each input and output under a controlled machine condition before enabling normal operation.

03

MANUFACTURER INSTRUCTIONS

A5E02486680-AP - V4.6, 11/2022

Read the complete safety, mounting, wiring and commissioning sections for the selected product and revision. Referenced accessories are not included unless expressly stated in the quotation.

04

BEFORE RETURN TO SERVICE

Verify the product label, configuration, protective measures and expected system response. Restore power in a controlled manner and complete the site functional test before normal operation.