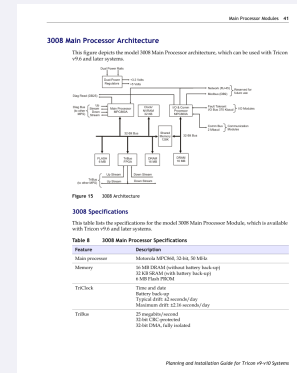


FIELD INSTALLATION QUICK GUIDE

Installation Instructions

Invensys Triconex 3008

TMR Main Processor Module



PRODUCT	3008
FAMILY	Tricon v9-v10
FUNCTION	TMR Main Processor Module
STATUS	Verify exact configuration

Prepared by Amikon Limited from Invensys 9700077-012, February 2009. This guide does not replace the complete manufacturer manual, site procedure, hazardous-area drawing, or approval document.

01 CONFIRM THE ORDERED CONFIGURATION

Main processor	Motorola MPC860, 32-bit, 50 MHz
DRAM	16 MB, without battery backup
SRAM	32 KB, with battery backup
Flash PROM	6 MB
TriClock function	Time and date with battery backup
TriClock drift	Typical +/-2 seconds/day; maximum +/-2.16 seconds/day
TriBus transfer rate	25 megabits/second, per model 3008 Table 8

02 MOUNTING AND CONNECTION

- Confirm 3008 identity and compatibility with the installed Tricon release.
- Verify processor firmware matches the other main processors.
- Check top slot key 007 and bottom slot key 002; never force insertion.
- Retain the validated application and configured retentive data records.
- Check Pass, Fault, Active, Maint1 and Maint2 against the 3008 status table.

OFFICIAL INSTALLATION REFERENCE

3008 Main Processor Architecture

This figure depicts the model 3008 Main Processor architecture, which can be used with Tricon v9.6 and later systems.

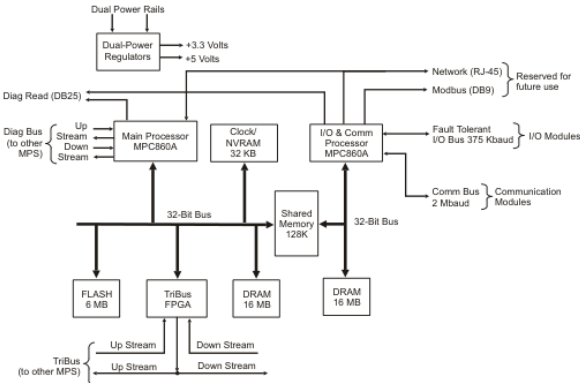


Figure 15 3008 Architecture

3008 Specifications

This table lists the specifications for the model 3008 Main Processor Module, which is available with Tricon v9.6 and later systems.

Table 8 3008 Main Processor Specifications	
Feature	Description
Main processor	Motorola MPC860, 32-bit, 50 MHz
Memory	16 MB DRAM (without battery back-up) 32 KB SRAM (with battery back-up) 6 MB Flash PROM
TriClock	Time and date Battery back-up Typical drift: ±2 seconds/day Maximum drift: ±2.16 seconds/day
TriBus	25 megabits/second 32-bit CRC-protected 32-bit DMA, fully isolated

Official manufacturer reference reproduced from Invensys 9700077-012, February 2009. Use the current source document and all referenced drawings and notes for installation.

04

CABLE ROUTING AND CONNECTION

- Verify processor firmware matches the other main processors.
- Check top slot key 007 and bottom slot key 002; never force insertion.
- Retain the validated application and configured retentive data records.
- Check Pass, Fault, Active, Maint1 and Maint2 against the 3008 status table.
- Verify configured SOE and I/O communication through the approved commissioning procedure.

05

INITIAL SYSTEM CHECKS

- Confirm model identity and compatible system components before energizing.
- Verify wiring, grounding, enclosure, and applicable hazardous-area requirements.
- Confirm normal indications and fault-contact state after commissioning.
- Record the installed order number and baseline operating condition.

06

CONTROLLED DOCUMENTS

Official source	Invensys 9700077-012, February 2009
Product record	3008
Manufacturer	Invensys Triconex
Support	INFO@AMIKON.CN