

OFFICIAL MANUFACTURER DOCUMENT PACKAGE

Manufacturer's Manual

6ES7314-6EH04-0AB0

Siemens official documentation for the exact article number.

Source: 6ES7314-6EH04-0AB0.pdf

Selected official Siemens pages follow this cover.

Preface

Purpose of this manual

This manual contains essential information about the following:

- Installation
- Communication
- Memory concept
- Cycle and response times
- Technical specifications of the CPUs.

Basic knowledge required

- In order to understand this manual, you require a general knowledge of automation engineering.
- You require knowledge of STEP 7 basic software.

Scope

The name CPU 31xC summarizes all compact CPUs, as table below shows:

CPU	Convention: CPU designations:	Order number	As of firmware version
CPU 312C	CPU 31xC	6ES7312-5BF04-0AB0	V3.3
CPU 313C		6ES7313-5BG04-0AB0	V3.3
CPU 313C-2 PtP		6ES7313-6BG04-0AB0	V3.3
CPU 313C-2 DP		6ES7313-6CG04-0AB0	V3.3
CPU 314C-2 PtP		6ES7314-6BH04-0AB0	V3.3
CPU 314C-2 DP		6ES7314-6CH04-0AB0	V3.3
CPU 314C-2 PN/DP		6ES7314-6EH04-0AB0	V3.3

The name CPU 31x summarizes all standard CPUs, as table below shows:

CPU	Convention: CPU designations:	Order number	As of firmware version
CPU 312	CPU 31x	6ES7312-1AE14-0AB0	V3.3
CPU 314		6ES7314-1AG14-0AB0	V3.3
CPU 315-2 DP		6ES7315-2AH14-0AB0	V3.3
CPU 315-2 PN/DP		6ES7315-2EH14-0AB0	V3.2
CPU 317-2 DP		6ES7317-2AK14-0AB0	V3.3
CPU 317-2 PN/DP		6ES7317-2EK14-0AB0	V3.2
CPU 319-3 PN/DP		6ES7318-3EL01-0AB0	V3.2

All CPUs with PROFINET properties are grouped under the designation CPU 31x PN/DP, as the following table shows:

CPU	Convention: CPU designations:	Order number	As of firmware version
CPU 314C-2 PN/DP	CPU 31x PN/DP	6ES7314-6EH04-0AB0	V3.3
CPU 315-2 PN/DP		6ES7315-2EH14-0AB0	V3.2
CPU 317-2 PN/DP		6ES7317-2EK14-0AB0	V3.2
CPU 319-3 PN/DP		6ES7318-3EL01-0AB0	V3.2

Note

A description of the special features of the failsafe CPUs of the S7 product range is available in the product information at the following Internet address
<http://support.automation.siemens.com/WW/view/en/11669702/133300>).

Note

We reserve the right to include a product Information containing the latest information on new modules or modules of a more recent version.

8.6 CPU 314C-2 PN/DP

Technical specifications

Table 8- 7 Technical specifications of the CPU 314C-2 PN/DP

Technical specifications	
CPU and version	
• MLFB	6ES7314-6EH04-0AB0
• Hardware version	01
• Firmware version	V3.3
• Associated programming package	STEP 7 as of V5.5 and HSP 191
Memory	
Main memory	
• Integrated	192 KB
• Expandable	No
• Maximum size of non-volatile memory for retentive data blocks	64 KB
Load memory	
• Pluggable (MMC)	Yes
• Pluggable (MMC), max.	8 MB
• Data retention on the Micro Memory Card (after the last programming action), min.	10 years
Backup	
• Available	Yes (ensured with Micro Memory Card - maintenance-free)
• Without battery	Yes (program and data)
Execution times	
• For bit operation, min.	0.06 µs
• For word operations, min.	0.12 µs
• For fixed-point arithmetic, min.	0.16 µs
• Minimum for floating-point arithmetic	0.59 µs
Timers/counters and their retentivity	
S7 counters	
• Number	256
Retentivity	
• Configurable	Yes
• Default	Z 0 to Z 7