

AMIKON TECHNICAL DOCUMENTATION

Installation Instructions

ICS Triplex T8121C

Trusted processor interface adapter

SERIAL PORTS	OPTION	POWER	DISSIPATION
3 x RS-422/485	IRIG-B decode	From processor	0.1 W

For qualified personnel only. This installation summary is not a substitute for the controlled manufacturer procedure, system safety plan or approved site isolation requirements.

01

BEFORE INSTALLATION

Exact identity	Confirm T8121C, hardware series and the required irig-b decode option.
Mounting	Match the rear T8100 processor positions and both active/standby connectors.
Electrical arrangement	Check processor-derived power, serial modes and the controlled connection drawing.
Bonding and condition	Maintain the documented earth bond. Use ESD precautions and reject damaged connectors.
Enabled functions	Verify IRIG-B format and termination; this variant does not enable Modbus Master.

02

PREPARATION AND BOUNDARIES

- Use the approved isolation and maintenance procedure before rear connector work; do not assume module replacement features authorize live adapter removal.
- Check the original installation figure and wiring tables. Maintain equipotential bonding and do not use this schematic as a pinout.

03
INSTALLATION AND COMMISSIONING

01	Record the full order code, actual series, required options and the approved processor configuration.
02	Identify the rear processor position from the original figure. Check both connectors and the retaining features.
03	Locate the adapter about 5 mm below its final position, engage both sides and raise it until the clips retain it.
04	Verify the processor-to-shell earth bond and cable screening against the original manufacturer instructions.
05	Match serial modes and termination to the network. Enable only the functions documented for the exact variant.
06	Verify processor health and the required communications or time signals under the approved commissioning procedure.

04
SAFETY AND FINAL CHECKS

- Processor serial ports are Modbus Slave only. Modbus Master requires the enabling adapter variant plus the separate communications interface.
- Check the actual processor revision: RS-485 biasing differs between T8111 and legacy T8110. Do not assume identical network behaviour.

05
OFFICIAL PROCEDURE

ICSTT-RM239J-EN-P | PD-T812X Issue 12

PDF pp. 11-13: construction; p. 17: installation; pp. 19-21: configuration and variants; p. 23: ratings.

Independent Amikon reference, not an OEM manual. Use the complete manufacturer instructions matching the actual hardware and firmware. No manufacturer pages are reproduced.

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06

ORIGINAL REFERENCE SCHEMATICS

ICS Triplex T8121C | Documented installation context

Original Amikon illustration based on the linked OEM pages. Logical arrangement only; not to scale and not an electrical wiring or manufacturer installation drawing.

A | EQUIPMENT ARRANGEMENT



Two processor connectors serve the active and standby positions. Maintain the documented adapter-to-chassis earth bond.

The option table, not connector appearance, determines whether IRIG-B decoding or Modbus Master is enabled.

B | CONFIGURATION BOUNDARY

Match the exact variant, actual hardware series and enabled functions. IRIG-B and Modbus Master are option-dependent; processor serial ports are Modbus Slave only.

07

ORIGINAL MANUFACTURER DRAWING AND PROCEDURE LINKS

[Adapter connector arrangement - PDF page 12](#)

[Correct rear installation position - PDF page 17](#)

[Variant options and master prerequisite - PDF page 21](#)

If the viewer ignores the page target, enter the stated PDF page manually. Retain the full manufacturer warnings and approved maintenance plan; this reference does not authorize live work.