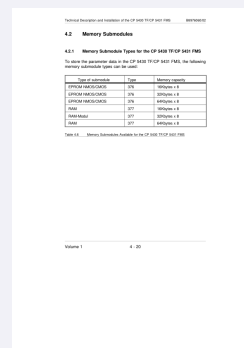


FIELD INSTALLATION QUICK GUIDE

# Installation Instructions

6GK1543-1AA01

PROFIBUS FMS communication processor



|          |                                                        |
|----------|--------------------------------------------------------|
| PRODUCT  | 6GK1543-1AA01                                          |
| FAMILY   | SIMATIC S5 CP 5431 FMS                                 |
| FUNCTION | PROFIBUS FMS communication processor                   |
| SOURCE   | Siemens SIMATIC S5 CP 5431 FMS technical documentation |

## 01 OFFICIAL MOUNTING AND WIRING

- Select a CP 5431 position supported by the installed SIMATIC S5 CPU and rack.
- Disconnect power before inserting or removing the open communications board.
- Use the documented RS485 and programming connectors; do not connect optical and electrical L2 interfaces simultaneously.
- Restore the COM 5431 FMS configuration and verify the intended FMS or DP communication service.

AMIKON PRODUCT SUPPORT

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#### 4.1.3.7 Interface Assignments

This section specifies the electrical interfaces of the CP 5430 TF/CP 5431 FMS (refer to Tables 4.3 - 4.5). The interfaces are as follows:

- Backplane connector X1/X2
- L2 interface socket X3
- PG interface socket X 4

| X1      | D           | B           | Z           | X2      | D           | B           | Z           |
|---------|-------------|-------------|-------------|---------|-------------|-------------|-------------|
| Pin no. | Signal name | Signal name | Signal name | Pin no. | Signal name | Signal name | Signal name |
| 2       | -           | M5          | P5          | 2       | -           | M5          | P5          |
| 4       | UBATT       | -           | -           | 4       | -           | -           | -           |
| 6       | ADB12       | ADB0        | -           | 6       | -           | -           | -           |
| 8       | ADB13       | ADB1        | MEMR        | 8       | -           | -           | -           |
| 10      | ADB14       | ADB2        | MEMW        | 10      | -           | -           | -           |
| 12      | ADB15       | ADB3        | RDY         | 12      | -           | -           | -           |
| 14      | IRA         | ADB4        | DB0         | 14      | -           | -           | NAU         |
| 16      | IRB         | ADB5        | DB1         | 16      | -           | -           | -           |
| 18      | IRC         | ADB6        | DB2         | 18      | -           | -           | -           |
| 20      | IRD         | ADB7        | DB3         | 20      | -           | -           | -           |
| 22      | -           | ADB8        | DB4         | 22      | TxD         | -           | -           |
| 24      | -           | ADB9        | DB5         | 24      | -           | -           | -           |
| 26      | -           | ADB10       | DB6         | 26      | -           | RxDs        | -           |
| 28      | ODSI        | ADB11       | DB7         | 28      | -           | -           | -           |
| 30      | -           | BASP        | -           | 30      | -           | -           | M24         |
| 32      | -           | M5          | -           | 32      | -           | M5          | P24         |

Table 4.3 Pinout Table of the Backplane Connectors X1 and X2

## 4.2 Memory Submodules

### 4.2.1 Memory Submodule Types for the CP 5430 TF/CP 5431 FMS

To store the parameter data in the CP 5430 TF/CP 5431 FMS, the following memory submodule types can be used:

| Type of submodule | Type | Memory capacity |
|-------------------|------|-----------------|
| EPROM NMOS/CMOS   | 376  | 16Kbytes x 8    |
| EPROM NMOS/CMOS   | 376  | 32Kbytes x 8    |
| EPROM NMOS/CMOS   | 376  | 64Kbytes x 8    |
| RAM               | 377  | 16Kbytes x 8    |
| RAM-Modul         | 377  | 32Kbytes x 8    |
| RAM               | 377  | 64Kbytes x 8    |

Table 4.6 Memory Submodules Available for the CP 5430 TF/CP 5431 FMS

## 4.3 Installation Guidelines

### 4.3.1 Basic Configuration

Fig. 4.6 illustrates the minimum configuration of a SINEC L2 bus system.

- Communications processor CP 5430 TF/CP 5431 FMS
- Bus terminal with terminal cable
- Bus cable

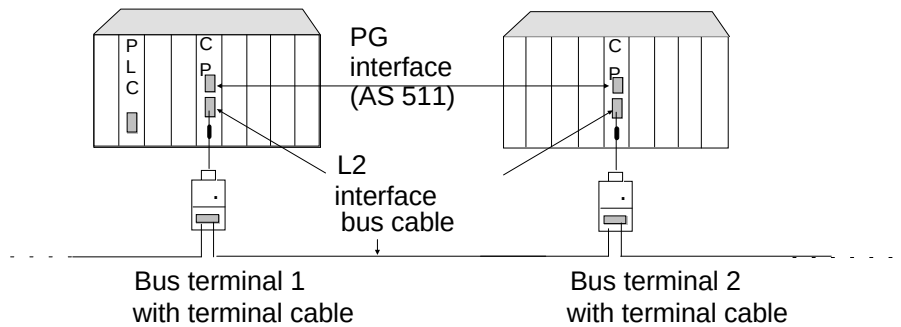


Fig. 4.6 SINEC L2 Components

#### 4.3.1.1 CP 5430 TF/CP 5431 FMS Slots in the various PLCs

The CP 5430 TF/CP 5431 FMS communications processor is designed as a board in double Eurocard format. The front panel is 1 1/3 standard slots wide.

The CP 5430 TF/CP 5431 FMS can be used in the CP slots of the PLCs listed in Section 4.1.2.

The module is supplied in a compact design and can be operated without a fan. If the module is to be inserted in the S5-115U, an adapter casing is required.