

MANUFACTURER DOCUMENT PACKAGE

Manufacturer's Manual

Reference package for procurement and maintenance review

GENERAL ELECTRIC

IC697CGR935

96 MHZ HOT-STANDBY CPU

Used with the applicable Series 90-70 configuration.

REFERENCE PACKAGE

OFFICIAL DOCUMENT

Selected official manufacturer pages follow this cover.

Source: GE Fanuc Controller and I/O Solutions catalog p.77 (PDF p.78); GFK-1440A release 7.60 notes, May 1 1998

IMPORTANT PRODUCT INFORMATION

READ THIS INFORMATION FIRST

Product: **PLC CPU Module, Release 7.60**
IC697CGR935-BA

The purpose of release 7.60 is to add GDB Redundancy to the IC697 CPU Redundancy Product and to level the CPU Redundancy (Currently at Release 4.7) up to Release 7.00 functionality. Please refer to the "Feature Restrictions" on page 18.

Release 7.60 is **ONLY** available for the following IC697 CPU: IC697CGR935.

Table 1. Identification of Release 7.60 CPU Hardware

Catalog Number	Board ID	Board Revision
IC697CGR935	CPXA1	44A739520-G01R04 or later (Motherboard)
	CMVB1	44A735200-G02R02 or later (Motherboard)

Table 2. Identification of Release 7.60 CPU Firmware

Catalog Number	Diskette Label	Checksum
IC697CGR935	44S750625-G01R01 (3")	n/a

Agency Approval

Starting with revision IC697CGR935-BA, this module has been granted UL Class I Div 2 and C-UL agency approval.

Packaging Note

The user's manual is not shipped with every product. User manuals are provided as a complete set in a library with the IC641SWP software and the IC641CTL software products, or are available on CD-ROM, or can be ordered as individual manuals.

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Special Operation Notes

IC641SWP Compatibility

This release of the PLC CPU modules is compatible with the version of the IC641SWP software (the MS-DOS® based programming package) listed in the table below. However, the IC641SWP software does **NOT** support Ethernet Global Data feature contained in the PLC CPU Release 7.60.

To work around a compatibility problem with the serial IC641SWP software, the PLC will return 3.70 as its version to all serial IC641SWP software products (WSI and Standard COMM) Release 3.04 and earlier.

Table 5. First Compatible IC641SWP Software Version

CPU Model	IC641SWP Software
CGR935	Version 7.01 or later

IC641CTL Compatibility

This release of the PLC CPU modules is compatible with the version of the IC641CTL software (the Windows® 95 and Windows NT® programming package) listed in the table shown below:

Table 6. First Compatible IC641CTL Version

CPU Model	IC641CTL Software
CGR935	Version 2.10 or later

Microcycle Mode

Microcycle mode was a new CPU sweep mode beginning with Release 6.00 of the IC697 CPU. Microcycle mode is not supported in this product.

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time for the Systems Communications Window to run or to configure the CPU sweep mode as **CONSTANT WINDOW** or **CONSTANT SWEEP** which will cause the CPU to cycle through the communications and background windows for approximately the same amount of time in both units.

Feature Restrictions

The following features are not supported in the redundant model CPU:

- I/O Interrupt and Timed Interrupt Program Blocks
- Periodic, Event-Triggered, and Timed Programs
- STOP/IOSCAN Mode
- Flash Memory Operation
- FIPI/O
- I/O Scan Sets (other than scan set 1)
- Microcycle Sweep Mode
- Serial Ports 1 and 2 *
- Release 7.10 Features

* *There are three serial ports on the CGR935.* Port 3 will have the same functionality as the single serial port for other IC697 CPU models. Ports 1 and 2 must be configured as **DISABLED**; failure to configure these ports as **DISABLED** may result in Unsupported Feature faults being logged at the time the configuration is downloaded to the PLC CPU.

The operation of the following features is different than with other IC697 CPUs:

- RUN/DISABLED Mode
- Configuration of Fault Actions
- STOP to RUN Transitions
- Default Background Window Time
- Ethernet Global Data Production

Release 7.60 of the redundant model CPU cannot be used to synchronize with Release 7.50. Dual bus application solutions running the GHS control strategy are not compatible with applications using the GDB control strategy; conversion to using the GDB control strategy will require that the application be halted and the logic/configuration changed accordingly.