MANUFACTURER DOCUMENT PACKAGE

Manufacturer's Manual

Selected official pages for procurement and maintenance review

PRODUCT BRAND

Siemens

PRODUCT MODEL

6ES7288-1ST30-0AA1

CPU ST30 Transistor Controller. Relevant section pages: 855, 856, 857, 858, 859, 860, 861, 862. Adjacent model text on shared pages is outside this product reference.

OFFICIAL SOURCE

S7-200 SMART System Manual; V2.8, 08/2023

Siemens original documentation and product reference

V2.8, 08/2023 technical excerpt for 6ES7288-1ST30-0AA1; read only the CPU ST30 DC/DC/DC column. Other model columns are outside scope. Not complete installation instructions.

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Amikon Limited prepared this independent reference package. Selected manufacturer pages follow with their original content and pagination visible.

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1.2 New Features

1.2.1 What's new in S7-200 SMART V2.8?

Only the following CPU models with firmware V2.8 support the new features described in this chapter:

Table 1-4 CPU models affected by firmware V2.8

CPU model	Article number
CPU SR20, AC/DC/Relay	6ES7288-1SR20-0AA1
CPU ST20, DC/DC/DC	6ES7288-1ST20-0AA1
CPU SR30, AC/DC/Relay	6ES7288-1SR30-0AA1
CPU ST30, DC/DC/DC	6ES7288-1ST30-0AA1
CPU SR40, AC/DC/Relay	6ES7288-1SR40-0AA1
CPU ST40, DC/DC/DC	6ES7288-1ST40-0AA1
CPU SR60, AC/DC/Relay	6ES7288-1SR60-0AA1
CPU ST60, DC/DC/DC	6ES7288-1ST60-0AA1

Note

CPU models with firmware V2.8 cannot be downgraded to V2.7 or any previous versions.

STEP 7-Micro/WIN SMART V2.8 release provides the following new features:

New instructions

- **Bit logic**
  - Alternating output and alternating output on pulse (Page 225)
- **Convert**
  - Double float to real (Page 274)
  - BCD to Double Integer (Page 274)
  - Double Integer to BCD (Page 274)
  - Scale (Page 291)

- **Math**
  - Get the maximum or minimum value (Page 343)
  - Get a random value (Page 344)
  - Sort the array (Page 345)
  - Get the average of a array (Page 347)
  - Get absolute values (Page 348)
  - Low-pass filter (Page 349)
  - ARCSIN, ARCCOS, ARCTAN (Page 339)
- **Logical operations**
  - NAND, NOR, and NXOR (Page 378)

HSC enhancement

- Support for high speed frequency counting (Page 297)

PID enhancement

- Support a maximum of 16 PID loops (Page 350)
- Support Bidirection PID (Page 352)
- Support PID pretuning (Page 714) and enhanced fine tuning (Page 716)

Motion enhancement

New capabilities about motion control are available as follows:

- For the CPU ST60:
  - The number of axis increases from three to four: Axis 3 (Page 742) is supported.
  - The number of axes group increases from one to two: Axes Group 1 (Page 810) is supported.
- For the CPU SR/ST 20, SR/ST 30, SR/ST 40 and SR/ST 60:
  - A new subroutine for axes group is supported: GRPx_STOP (Page 823).
    The GPRx_STOP subroutine triggers a decelerated stop command or an immediate stop command to stop the axes group movement.
  - Configuring the move path parameters dynamically (Page 810) is supported. This means that you don't need to modify parameters in motion wizard and download the project again.

PLS and PWM enhancement for ST60

The number of pulse output (PLS) increases from three to four: the fourth PLS instruction (Page 324) is supported.

The number of pulse width modulated (PWM) increases from three to four: the fourth PWM instruction (Page 328) is supported.

Enlarged memory ranges

For the CPU SR/ST 20, SR/ST 30, SR/ST 40 and SR/ST 60, the variable memory (Page 998) increases by 4KB separately.

For the CPU SR/ST 20, SR/ST 30, SR/ST 40 and SR/ST 60, the retentive range (Page 155) increases by 2KB separately.

Security enhancement

- PLC Security enhancement (Page 156): Use more sophisticated and efficient algorithms to protect the security of PLC.
- POU Security enhancement (Page 39): Use more powerful algorithms to protect the security of POU.

1.2.2 What's new in S7-200 SMART V2.7?

Only the following CPU models with firmware V2.7 support the new features described in this chapter:

Table 1-5 CPU models affected by firmware V2.7

CPU model	Article number
CPU SR20, AC/DC/Relay	6ES7288-1SR20-0AA1
CPU ST20, DC/DC/DC	6ES7288-1ST20-0AA1
CPU SR30, AC/DC/Relay	6ES7288-1SR30-0AA1
CPU ST30, DC/DC/DC	6ES7288-1ST30-0AA1
CPU SR40, AC/DC/Relay	6ES7288-1SR40-0AA1
CPU ST40, DC/DC/DC	6ES7288-1ST40-0AA1
CPU SR60, AC/DC/Relay	6ES7288-1SR60-0AA1
CPU ST60, DC/DC/DC	6ES7288-1ST60-0AA1

Note

CPU models with firmware V2.7 cannot be downgraded to V2.6 or any previous versions.

Note

Any previous SR or ST model with an article number ending with 0AA0 cannot be upgraded to V2.7.

CPU models with an article number ending with 0AA1 cannot be downgraded to any previous firmware versions.

STEP 7-Micro/WIN SMART V2.7 release provides the following new features:

A.2.2 CPU ST30, CPU SR30, and CPU CR30s

A.2.2.1 General specifications and features

General specifications and features of the CPU ST30, CPU SR30 and CPU CR30s

Table A-24 General specifications

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Article number	6ES7288-1ST30-0AA1	6ES7288-1SR30-0AA1	6E88288-1CR30-0AA1
Dimensions W x H x D (mm)	110 x 100 x 81	110 x 100 x 81	110 x 100 x 81
Weight	375 g	435 g	424 g
Power dissipation	12 W	14 W	7 w
Current available (EM bus)	1400 mA max. (5 V DC)	1400 mA max. (5 V DC)	Not available
Current available (24 V DC)	300 mA max. (sensor power)	300 mA max. (sensor power)	300 mA max. (sensor power)
Digital input current consumption (24 V DC)	4 mA/ input used	4 mA/ input used	4 mA/ input used

Table A-25 CPU features

Technical data		CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
User memory[1]	Program	18 Kbytes	18 Kbytes	12 Kbytes
	User data (V)	16 Kbytes	16 Kbytes	8 Kbytes
	Retentive	12 Kbytes max.[1]	12 Kbytes max.[1]	2 Kbytes max.[1]
On-board digital I/O		18 inputs/12 outputs	18 inputs/12 outputs	18 inputs/12 outputs
Process image		256 bits of inputs (I) / 256 bits of outputs (Q)	256 bits of inputs (I) / 256 bits of outputs (Q)	256 bits of inputs (I) / 256 bits of outputs (Q)
Analog image		56 words of inputs (AI) / 56 words of outputs (AQ)	56 words of inputs (AI) / 56 words of outputs (AQ)	Not available
Bit memory (M)		256 bits	256 bits	256 bits
Temporary (local) memory (L)		64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)	64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)	64 bytes in the main program and 64 bytes in each subroutine and interrupt routine 60 bytes when programming in LAD or FBD (STEP 7-Micro/WIN reserves 4 bytes)
Sequential control relays (S)		256 bits	256 bits	256 bits
Expansion modules expansion		6	6	Not available
Signal board expansion		1 max.	1 max.	Not available

Technical data		CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
High-speed counters	Total	6	6	4
	Single phase	5 at 200 kHz 1 at 30 kHz	5 at 200 kHz 1 at 30 kHz	4 at 100 kHz
	A/B phase	3 at 100 kHz 1 at 20 kHz	3 at 100 kHz 1 at 20 kHz	2 at 50 kHz
Pulse outputs [2]		3 at 100 kHz	--	Not available
Pulse catch inputs		12	12	Not available
Cyclic interrupts		2 at 1 ms resolution	2 at 1 ms resolution	2 at 1 ms resolution
Edge interrupts		4 rising and 4 falling (6 and 6 with optional signal board)	4 rising and 4 falling (6 and 6 with optional signal board)	4 rising and 4 falling
Memory card		microSDHC Card (optional)	microSDHC Card (optional)	Not available
Real time clock accuracy		+/- 120 seconds/month	+/- 120 seconds/month	Not available
Real time clock retention time		7 days typ./6 days min. at 25 °C (maintenance-free Super Capacitor)	7 days typ./6 days min. at 25 °C (maintenance-free Super Capacitor)	Not available

[1] You can configure areas of V memory, M memory, C memory (current values), and portions of T memory (current values on retentive times) to be retentive, up to the specified maximum amount.

[2] The specified maximum pulse frequency is possible only for CPU models with transistor outputs. Pulse output operation is not recommended for CPU models with relay outputs.

Table A-26 PROFINET features

Description	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay
Support Controller function for PROFINET communication	Yes	
Support I-Device function for PROFINET communication	Yes	
Maximum number of PROFINET device	8	
Device number of PROFINET device	1 to 8	
Maximum input size of each PROFINET device	128 Bytes	
Maximum output size of each PROFINET device	128 Bytes	
Maximum input size of PROFINET I-Device	128 Bytes	
Maximum output size of PROFINET I-Device	128 Bytes	
Maximum number of modules	64	
Minimum cyclic update time of PROFINET device	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	
CPU address range of PROFINET process image input register	I128.0 to I1279.7	
CPU address range of PROFINET process image output register	Q128.0 to Q1279.7	
CPU address of PROFINET process image input and output register for device #1	I128.0 to I255.7 Q128.0 to Q255.7	
CPU address of PROFINET process image input and output register for device #2	I256.0 to I383.7 Q256.0 to Q383.7	

Description	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay
CPU address of PROFINET process image input and output register for device #3	I384.0 to I511.7 Q384.0 to Q511.7	
CPU address of PROFINET process image input and output register for device #4	I512.0 to I639.7 Q512.0 to Q639.7	
CPU address of PROFINET process image input and output register for device #5	I640.0 to I767.7 Q640.0 to Q767.7	
CPU address of PROFINET process image input and output register for device #6	I768.0 to I895.7 Q768.0 to Q895.7	
CPU address of PROFINET process image input and output register for device #7	I896.0 to I1023.7 Q896.0 to Q1023.7	
CPU address of PROFINET process image input and output register for device #8	I1024.0 to I1151.7 Q1024.0 to Q1151.7	
CPU address of PROFINET process image input and output register for I-Device	I1152.0 to I1279.7 Q1152.0 to Q1279.7	

Table A-27 Performance

Type of instruction	Execution speed
Boolean	150 ns instruction
Move Word	1.2 µs/instruction
Real math	3.6 µs/instruction

Table A-28 User program elements supported

Element		Description
POUs	Type/quantity	Main program: 1 Subroutines: 128 (0 to 127) Interrupt routines: 128 (0 to 127)
	Nesting depth	From main program: 8 subroutine levels From interrupt routine: 4 subroutine levels
Accumulators	Quantity	4
Timers	Type/quantity	Non-retentive (TON, TOF): 192 Retentive (TONR): 64
Counters	Quantity	256

Table A-29 Communication

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Number of ports	PROFINET (LAN): 1 Serial ports: 1 (RS485) Add-on serial ports: 1 (with optional RS232/485 signal board)	PROFINET (LAN): 1 Serial ports: 1 (RS485) Add-on serial ports: 1 (with optional RS232/485 signal board)	PROFINET (LAN): 0 Serial ports: 1 (RS485) Add-on serial ports: 0
HMI device	PROFINET (LAN): 8 connections Serial ports: 4 connections per port	PROFINET (LAN): 8 connections Serial ports: 4 connections per port	PROFINET (LAN): Not available Serial ports: 4 connections per port
Programming device (PG)	PROFINET (LAN): 1 connection Serial ports: 1 connection	PROFINET (LAN): 1 connection Serial ports: 1 connection	PROFINET (LAN): Not available Serial ports: 1 connection
CPUs (PUT/GET)	PROFINET (LAN): 8 client and 8 server connections	PROFINET (LAN): 8 client and 8 server connections	PROFINET (LAN): Not available
Open user communication	PROFINET (LAN): 8 active and 8 passive connections	PROFINET (LAN): 8 active and 8 passive connections	PROFINET (LAN): Not available
Data rates	PROFINET (LAN): 10/100 Mb/s RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s	PROFINET (LAN): 10/100 Mb/s RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s	PROFINET (LAN): Not available RS485 system protocols: 9600, 19200, and 187500 b/s RS485 freeport: 1200 to 115200 b/s
Isolation (external signal to PLC logic)	PROFINET (LAN): Transformer isolated, 1500 V AC RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;	PROFINET (LAN): Transformer isolated, 1500 V AC RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;	PROFINET (LAN): Not available RS485: RS485 signal to chassis ground, 500 V AC/ 707 V DC; RS485 signal to CPU logic common, 500 V AC/ 707V DC;
Cable type	PROFINET (LAN): CAT5e shielded RS485: PROFIBUS network cable	PROFINET (LAN): CAT5e shielded RS485: PROFIBUS network cable	PROFINET (LAN): Not available RS485: PROFIBUS network cable
PROFINET Communication			
PROFINET controller	Yes	Yes	No
PROFINET device	Yes	Yes	No
PROFINET controller			
Services			
PG/OP communication	Yes	Yes	No
S7 routing	Yes	Yes	No
Isochronous mode	No	No	No
Open IE communication	Yes	Yes	No
IRT	No	No	No
MRP	No	No	No
PROFIenergy	No	No	No

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Max. number of PROFINET devices that you can connect for RT	8	8	--
Max. number of module	64	64	--
Update times	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	The minimum value of the update time also depends on the communication component set for PROFINET, on the number of PROFINET devices, and the quantity of configured user data.	No
With RT			
Send clock of 1 ms	1 ms to 512 ms	1 ms to 512 ms	--

Table A-30 Power supply

Technical data		CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay		CPU CR30s AC/DC/Relay	
Voltage range		20.4 to 28.8 V DC	85 to 264 V AC	77 to 138 V DC	85 to 264 V AC	77 to 138 V DC
Line frequency		--	47 to 63 Hz	--	47 to 63 Hz	--
Input current	CPU only at max. load	64 mA at 24 V DC (without driving 300 mA sensor power) 365 mA at 24 V DC (with driving 300 mA sensor power)	92 mA at 120 V AC (with power sensor) 40 mA at 120 V AC (without power sensor) 52 mA at 240 V AC (with power sensor) 27 mA at 240 V AC (without power sensor)	122 mA at 110 VDC (with 300 mA power sensor output) 46 mA at 110 VDC (without 300 mA power sensor output)	90 mA at 120 V AC 70 mA at 240 V AC	122 mA at 110 VDC (with 300 mA power sensor output) 46 mA at 110 VDC (without 300 mA power sensor output)
	CPU with all expansion accessories at max. load	624 mA at 24 V DC	136 mA at 120 V AC 72 mA at 240 V AC	208 mA at 110 VDC (with 300 mA power sensor output)	--	208 mA at 110 VDC (with 300 mA power sensor output)
Hold up time (loss of power)		20 ms at 24 V DC	30 ms at 120 V AC 200 ms at 240 V AC	30 ms at 110 V DC	30 ms at 120 V AC 200 ms at 240 V AC	30 ms at 110 V DC
Inrush current (max.)		6A at 28.8 V DC	8.9 A at 264 V AC		16.3 A at 264 V AC	
Isolation (input power to logic)		--	1500 V AC		1500 V AC	

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Ground leakage, AC line to functional earth	--	0.5 mA max.	0.5 mA max.
Internal fuse, not user replaceable	3 A, 250 V, slow blow	3 A, 250 V, slow blow	3 A, 250 V, slow blow

Table A-31 Sensor power

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Voltage range	20.4 to 28.8 V DC	20.4 to 28.8 V DC	20.4 to 28.8 V DC
Output current rating (max.)	300 mA (short circuit protected)	300 mA (short circuit protected)	300 mA (short circuit protected)
Maximum ripple noise (<10 MHz)	< 1 V peak to peak	< 1 V peak to peak	< 1 V peak to peak
Isolation (CPU logic to sensor power)	Not isolated	Not isolated	Not isolated

A.2.2.2 Digital inputs and outputs

Table A-32 Digital inputs

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Number of inputs	18	18	18
Type	Sink/Source (IEC Type 1 sink, except I0.0 to I0.3, I0.6 to I0.7)	Sink/Source (IEC Type 1 sink)	Sink/Source (IEC Type 1 sink)
Rated voltage	24 V DC at 4 mA, nominal	24 V DC at 4 mA, nominal	24 V DC at 4 mA, nominal
Continuous permissible voltage	30 V DC, max	30 V DC, max.	30 V DC, max.
Surge voltage	35 V DC for 0.5 sec.	35 V DC for 0.5 sec.	35 V DC for 0.5 sec.
Logic 1 signal (min.)	I0.0 to I0.3, I0.6 to I0.7: 4 V DC at 8 mA Other inputs: 15 V DC at 2.5 mA	15 V DC at 2.5 mA	15 V DC at 2.5 mA
Logic 0 signal (max.)	I0.0 to I0.3, I0.6 to I0.7: 1 V DC at 1 mA Other inputs: 5 V DC at 1 mA	5 V DC at 1 mA	5 V DC at 1 mA
Isolation (field side to logic)	500 V AC for 1 minute	500 V AC for 1 minute	500 V AC for 1 minute
Isolation groups	1	1	1

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Filter times	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8	Individually selectable on each channel (points I0.0 to I1.5): µs: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8 ms: 0.2, 0.4, 0.8, 1.6, 3.2, 6.4, 12.8
	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8	Individually selectable on each channel (points I1.6 and greater): ms: 0, 6.4, 12.8
HSC clock input rates (max.) (Logic 1 Level = 15 to 26 V DC)	Single phase: 5 HSCs at 200 kHz; 1 HSC at 30 kHz A/B phase: 3 HSCs at 100 kHz; 1 HSC at 20 kHz	Single phase: 5 HSCs at 200 kHz; 1 HSCs at 30 kHz A/B phase: 3 HSCs at 100 kHz; 1 HSC at 20 kHz	Single phase: 4 HSCs at 100 kHz A/B phase: 2 HSCs at 50 kHz
Number of inputs on simultaneously	18	18	18
Cable length (max.), in meters	I0.0 to I0.3: Shielded (only): • 500 m normal (low-speed) inputs • 50 m HSC (high-speed) inputs	All inputs: • Shielded: 500 m normal inputs, 50 m HSC inputs • Unshielded: 300 m normal inputs	All inputs: • Shielded: 500 m normal inputs, 50 m HSC inputs • Unshielded: 300 m normal inputs
	I0.6 to I0.7: • Shielded (only): 500 m normal inputs		
	All other inputs: • Shielded: 500 m normal inputs • Unshielded: 300 m normal inputs[1]		

[1] When I0.0 to I0.3 are used at high-speed counter inputs, all other inputs must use shielded cable.

Table A-33 Digital outputs

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Number of outputs	12	12	12
Type	Solid state - MOSFET (sourcing)	Relay, dry contact	Relay, dry contact
Voltage range	20.4 to 28.8 V DC	5 to 30 V DC or 5 to 250 V AC	5 to 30 V DC or 5 to 250 V AC
Logic 1 signal at max. current	20 V DC min.	--	--
Logic 0 signal with 10 KΩ load	0.1 V DC max.	--	--
Rated current per point (max.)	0.5 A	2.0 A	2.0 A

Technical data	CPU ST30 DC/DC/DC	CPU SR30 AC/DC/Relay	CPU CR30s AC/DC/Relay
Rated current per common (max.)	6 A	10.0 A	10.0 A
Lamp load	5 W	30 W DC/200 W AC	30 W DC/200 W AC
ON state resistance	0.6 Ω max.	0.2 Ω max. when new	0.2 Ω max. when new
Leakage current per point	10 µA max.	--	--
Surge current	8 A for 100 ms max.	7 A with contacts closed	7 A with contacts closed
Overload protection	No	No	No
Isolation (field side to logic)	500 V AC for 1 minute	1500 V AC for 1 minute (coil to contact) None (coil to logic)	1500 V AC for 1 minute (coil to contact) None (coil to logic)
Isolation resistance	--	100 M Ω min. when new	100 M Ω min. when new
Isolation between open contacts	--	750 V AC for 1 minute	750 V AC for 1 minute
Isolation groups	1	1	1
Inductive clamp voltage	L+ minus 48 V DC, 1 W dissipation	Not recommended	Not recommended
Switching delay (Qa.0 to Qa.3)	1.0 µs max., off to on 3.0 µs max., on to off	10 ms max.	10 ms max.
Switching delay (Qa.4 to Qb.7)	50 µs max., off to on, 200 µs max., on to off	10 ms max.	10 ms max.
Maximum relay switching frequency	--	1 Hz	1 Hz
Lifetime mechanical (no load)	--	10,000,000 open/close cycles	10,000,000 open/close cycles
Lifetime contacts at rated load	--	100,000 open/close cycles	100,000 open/close cycles
Output state in STOP mode	Last value or substitute value (default value 0)	Last value or substitute value (default value 0)	Last value or substitute value (default value 0)
Number of outputs on simultaneously	12	12	12
Cable length (max.), in meters	Shielded: 500 m Unshielded: 150 m	Shielded: 500 m Unshielded: 150 m	Shielded: 500 m Unshielded: 150 m