AMIKON TECHNICAL DOCUMENTATION

Product Data Sheet

Siemens 6ES7288-1ST60-0AA0

CPU ST60 Transistor Controller



DEVICE	ONBOARD I/O	PROGRAM	SUPPLY
CPU ST60	**36 DI / 24 DO**	**30 KB**	**20.4-28.8 V DC**

Siemens 6ES7288-1ST60-0AA0 CPU ST60 combines thirty-six digital inputs with twenty-four sourcing transistor outputs. The original standard SMART hardware provides 30 KB of program memory and three designated pulse-output resources; the V2.5 technical table identifies a separate 20 KB variable-memory area.

01 VERIFIED TECHNICAL INFORMATION

CPU supply range	20.4 to 28.8 V DC
Onboard digital inputs	36, nominal 24 V DC
Onboard digital outputs	24
Output technology	Sourcing MOSFET transistor
Program memory	30 KB
Ethernet interface	1, 10/100 Mbit/s
Serial interface	1 x RS485

Manufacturer evidence: S7-200 SMART System Manual; exact-model manual section.

02 TECHNICAL INFORMATION CONTINUED

Expansion modules	6 maximum
Signal-board expansion	1 maximum
Sensor-power output	24 V DC, 300 mA maximum
Output current per point	0.5 A maximum; observe common-group and load-type limits
Expansion-bus supply	5 V DC, 1400 mA maximum
Pulse outputs	3 designated channels, up to 100 kHz

03 SOURCE AND CONFIGURATION CHECKS

A 175 mm-wide housing accommodates the local I/O and standard expansion connections. Ethernet and RS485 serve the communication configuration, while the six-module expansion capacity, optional signal board and separate sensor-power budget provide distinct planning limits for a station built around this original ST60 hardware.

Packed dimensions and supplied-unit origin must be checked against the actual delivery. No verified gross shipping weight is inferred from module net mass.

Siemens original documentation and product reference

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